

**Amendments to the Claims:**

This listing of claims will replace all prior versions and listings of claims in the application:

**Listing of the Claims:**

1. (Currently Amended) A method comprising:

determining identifying a power state of a first system, the power state to be one of at least a first and second power states, the second power state to consume less power than the first power state; and

in response to the system being in the second power state, switching,  
without using a main operating system, a serial Advanced Technology Attachment (SATA) link from the first system to a link with an autonomous subsystem.
2. (Previously Presented) The method according to claim 1, wherein the power state is power state of an Advanced Configuration Power Interface Specification (ACPI S) state.
3. (Canceled)
4. (Canceled)

5. (Previously Presented) The method according to claim 2, wherein:
  - if the ACPI state is S0, S1, or S2 then the SATA is switched to the first system; and
    - if the ACPI state is S3, S4, or S5 then the SATA is switched to the subsystem.
6. (Previously Presented) The method according to claim 2, wherein:
  - if the ACPI state is S0, or S1 then the SATA is switched to the first system;
  - and
    - if the ACPI state is S2, S3, S4, or S5 then the SATA is switched to the subsystem.
7. (Currently Amended) A machine-readable medium having stored thereon instructions, which when executed by a processor, causes said processor to perform the following:

determine identify a power state of a first system, the power state to be at least one of a first and second power states, the second power state to consume less power than the first power state; and

in response to the system being in the second power state, switch, without use of a main operating system, a serial Advanced Technology Attachment (SATA) link from the first system to a link with an autonomous subsystem.

8. (Canceled)
9. (Currently Amended) A system comprising:
  - a serial Advanced Technology Attachment (SATA) device connected to a switch;
  - a switch to connect the system to the SATA device when the system is in a first power state, and the switch to connect an autonomous subsystem to the SATA device, without use of a main operating system, when the system is in a second power state, the second power state to consume less power than the first power state.
10. (Previously Presented) The system of claim 9, wherein the switch connecting the SATA device does not connect both the system and the subsystem to the SATA device simultaneously.
11. (Previously Presented) The system of claim 9, wherein the switch operation is controlled by signals from the system.
12. (Currently Amended) An apparatus comprising:
  - means for determining identifying a power state of a first system, the power state to be one of at least a first and second power states, the second power state to consume less power than the first power state; and

means for switching, without use of a main operating system, a serial Advanced Technology Attachment (SATA) link from the first system to a link with an autonomous subsystem in response to determining the first system is in the second power state.

13. (Original) The apparatus of claim 12, wherein means for switching further comprises a mutually exclusive switching means to a plurality of destinations.

14. (Previously Presented) The apparatus of claim 12, wherein the power state is power state of an Advanced Configuration Power Interface Specification (ACPI) state.

15. (Previously Presented) The apparatus of claim 12, wherein the means for switching the SATA determined whether to switch based upon signals from the first system.